

Amendments to the Specification

Please amend the paragraph beginning on page 10, line 31 as follows:

The phase combining circuit 103a receives, for example, four-phase clocks (reference clocks) $\Phi 0$, $\Phi 0X$, $\Phi 1$, and $\Phi 1X$ and the outputs of the D/A converter 403d 103b, applies weights WS (the outputs of the D/A converter ~~403d~~ 103b) to the respective reference clocks, and thereby outputs through the amplifying circuit 103c a clock Θ (ΘX) having a phase intermediate between the respective reference clocks. Here, the four-phase clocks are of the same frequency but differ in phase by 90° relative to each other; that is, the clock $\Phi 0$ has a phase difference of 180° relative to the clock $\Phi 0X$, and likewise, the clock $\Phi 1$ has a phase difference of 180° relative to the clock $\Phi 1X$.